

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	3647	powerpc	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 11:01
S2	73	powerpc and length adj field	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/24 16:59
S3	0	powerpc and instruction adj length adj field	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/24 16:59
S4	9	powerpc and (instruction same (length adj field))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/24 16:59
S5	183	hardware adj2 transaction	US-PGPUB; USPAT; IBM_TDB	OR	ON	2004/08/25 09:48
S6	0	hardware adj2 transaction same redefinition	US-PGPUB; USPAT; IBM_TDB	OR	ON	2004/08/25 09:48
S7	0	hardware adj2 transaction same redefine	US-PGPUB; USPAT; IBM_TDB	OR	ON	2004/08/25 09:48
S8	1	hardware adj2 transaction same redirection	US-PGPUB; USPAT; IBM_TDB	OR	ON	2004/08/25 09:57
S9	69	"4,454,578"	US-PGPUB; USPAT; IBM_TDB	OR	ON	2004/08/25 09:57
S10	15	"5790843"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/26 16:10
S11	4	"instruction match register"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/26 14:24

S12	335	VLIW and "variable length"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/26 14:24
S13	5040	"enable/disable"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/26 16:59
S14	522	"enable/disable signal"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/26 16:52
S15	6	"enable/disable signal" and "712"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/26 16:12
S16	2	"6427202"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/26 16:52
S17	1586	"enable/disable" and instruction	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/26 16:59
S18	0	"enable/disable" and instruction adj redefinition	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/26 17:00
S19	5	instruction adj redefinition	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/26 17:00
S20	18	goes adj in adj center	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/30 11:11
S21	5	10/045564	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/30 11:10

S22	12	("4095278" "4109311" "4439828" "4542453" "4802119" "4974147" "5042005" "5075845" "5390304" "5426746" "5448744" "5473754").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/01/27 10:10
S23	124	transaction with lookup with table	US-PGPUB; USPAT; USOCR	OR	OFF	2005/01/27 10:11
S24	0	S23 and "712"/\$.ccls	US-PGPUB; USPAT; USOCR	OR	OFF	2005/01/27 10:39
S25	8	transaction adj lookup adj table	US-PGPUB; USPAT; USOCR	OR	OFF	2005/01/27 10:23
S26	343	712/226.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/01/27 12:17
S27	9	S23 and ("712"/\$.ccls or "711"/\$.ccls.)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/01/27 10:42
S28	103	712/226.ccls. and (multiplexer or multiplexor)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/05 12:45
S29	17	712/226.ccls. and (multiplexer or multiplexor) and comparator	US-PGPUB; USPAT; USOCR	OR	OFF	2005/01/27 10:42
S30	0	transaction with table and (mux or multiplexer or multiplexor)	EPO; JPO; IBM_TDB	OR	OFF	2005/01/27 12:18
S31	656	transaction with table	EPO; JPO; IBM_TDB	OR	OFF	2005/01/27 12:18
S32	0	transaction with lookup with table	EPO; JPO; IBM_TDB	OR	OFF	2005/01/27 12:18
S33	3	S31 and alternat\$3	EPO; JPO; IBM_TDB	OR	OFF	2005/01/27 12:20
S34	0	transaction and redirection and specification and changes	EPO; JPO; IBM_TDB	OR	OFF	2005/01/27 12:20
S35	0	transaction and redirection and specification	EPO; JPO; IBM_TDB	OR	OFF	2005/01/27 12:20
S36	4	transaction and redirection	EPO; JPO; IBM_TDB	OR	OFF	2005/01/27 12:21
S37	12	transaction with response with register	EPO; JPO; IBM_TDB	OR	OFF	2005/01/27 12:21
S38	119	instruction adj patch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/02 13:49

S39	1	flexibility near4 hardwired adj logic	IBM_TDB	OR	OFF	2005/05/02 13:51
S40	4	architectural adj changes	IBM_TDB	OR	OFF	2005/05/02 13:52
S41	0	architectural adj changes	EPO; JPO	OR	OFF	2005/05/02 13:52
S42	288	architectural adj changes	US-PGPUB; USPAT	OR	OFF	2005/05/02 13:52
S43	2247	comparator and (look adj up adj table) and multiplexer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/05 12:32
S44	1680	comparator and (look adj up adj table) and multiplexer and register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/05 12:35
S45	280	comparator and (look adj up adj table) and multiplexer and register and alter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/05 12:35
S46	182	instruction adj modification	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/05 13:03
S47	354	decode adj table	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/05 13:19
S48	345	decode with (look adj up adj table)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/05 14:52
S49	11	(target adj memory adj address) and (speculative with execution)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/05 15:59
S50	1	"5625835".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/05 15:59
S51	10	("4903264" "5450560" "5463745" "5526499" "5537559" "5542075" "5546599" "5548738" "5557763" "5559976").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/05 16:01